



MWENDWA AUGUSTINE MUTINDA

FPGA Developer/ Hardware Engineer

TechStack:

Programming Languages: VHDL, Python, C, C++, Micropython, Nodejs Software Tools: MATLAB, Vivado, Vitis, Cadence, AutoCAD, Proteus, VsCode Hardware Tools: FPGA (HDL), STM32, PIC, Raspberry Pi, Arduino, Fusion360

Short Resume:

Position: FPGA Developer at SEN, Germany, focusing on high-speed data acquisition and signal processing

Skills: Expert in FPGA programming, hardware programming, PCB design, PCB Assembly line, electrical system network design, and evaluation

Experience: Held roles at SEN, Code For Africa (Sensors Africa), Wote Technical Training Institute with achievements in hardware programming and electrical design

Motivation: Augustine's motivation to move to Germany as a software developer stems from its thrivingsoftware developer stems from its thriving tech ecosystem, focus on AI and blockchain, and opportunities for professional growth. The country's work-life balance, commitment to sustainability, and multicultural environment align with his values and aspires him to contribute to cutting-edge, innovative projects.

Starting in Germany: After 6 - 8 Weeks

BlueCard eligible: Yes

Starting Salary: €80.000
University degree:
Bsc In Electrical And
Electronics Engineering

English: C1 German: B1

Tel: +491748856183

LinkedIn

linkedin.com/in/augustine-mwendwab00420145/

E-Mail:

augustine.mwendwa@candidate. talents2germany.de

www.Talents2Germany.de